**PATENT** 

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Applica	ation of:	)		
RUMENNIK, et al.		)	Examiner:	Hu, Shouxiang
Serial No.:	09/961,229	)	Art Unit:	2811
Filing Date:	September 20, 2001	)		
	OLTAGE TRANSISTOR WITH LAYER CONDUCTION REGION	) ) )		

## **Submission of Supplemental Declaration**

Mail Stop Issue Fee Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

In accordance with 37 CFR § 1.67 and MPEP 603, Applicant is submitting herewith a supplemental declaration for the above-referenced application, which lists three joint inventors. The attached supplemental declaration is signed by the available joint inventors, Vladimir Rumennik and Donald Disney, on behalf of themselves and the unavailable joint inventor, J.S. Ajit. Mr. Ajit, who is no longer employed by the assignee (Power Integrations, Inc.) of the above application, could not be found to execute the supplemental declaration. The undersigned attorney of record unsuccessfully attempted to contact Mr. Ajit by mailing correspondence to his last known address, 455-E Costa Mesa Terrace, Sunnyvale, California 94086, and by calling the local telephone company's directory assistance service only to learn that is no Janardhanan S. Ajit listed in the general area code vicinity.

Accordingly, Applicant respectfully requests that the attached supplemental declaration be placed in the application file pursuant to 37 CFR §§ 1.67, 1.183 and MPEP 603.

Please charge any shortages of fees or credit any overcharges of fees to our Deposit Account No. 50-2060.

Respectfully submitted,

BURGESS & BEREZNAK, LLP

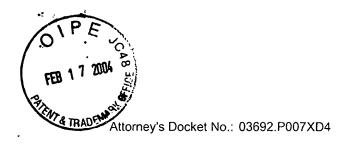
Dated: \_\_\_\_\_\_\_, 2004

Bradley J. Bereznak Attorney for Applicant Registration No. 33,474

800 West El Camino Real Suite 180 Mountain View, CA 94040 (650) 903-2264 I hereby certify that this correspondence is being deposited with th United States Postal Service as first class mail with sufficient posta in an envelope addressed to the Commissioner for Patents, M/S Issue Fee, P.O. Box 1450, Alexandria, VA 22313-1450 on February 11, 2004.

Caitlin Burgess

February 11, 2004



**PATENT** 

## SUPPLEMENTAL DECLARATION FOR PATENT APPLICATION

As a below named inventor, I hereby declare that: My residence, post office address, and citizenship are as stated below, next to my name. I believe I am the original, first, and sole inventor (if only one name is listed below) or an original, first, and joint inventor (if plural names are listed below) named in the patent application entitled HIGH-VOLTAGE TRANSISTOR WITH MULTI-LAYER CONDUCTION REGION the specification of which was filed on 09/20/01 as application Serial No. 09/961,229. I hereby declare that the subject matter of the claim(s) as allowed of the attached amendment(s) of the amendment(s) filed on or about \_\_\_\_ in the above-identified application of the amendment(s) filed on or about \_\_\_\_ in application Serial No. \_\_ filed \_\_\_\_\_, which is a continuation divisional of application Serial No. \_\_ filed \_\_\_\_\_, was part of my invention and was invented before the filing of the following application for such invention: XX U.S. patent application Serial No. 09/245,030 , filed 02/05/99 prior foreign application no. I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims, as allowed, or as amended by any amendment referred to above. I acknowledge the duty to disclose all information known to me to be material to patentability as defined in Title 37, Code of Federal Regulations, Section 1.56. (Appendix A)

03692.P007XD4

which priority is claimed:

I hereby claim foreign priority benefits under Title 35, United States Code, Section 119(a)-(d), of any foreign application(s) for patent or inventor's certificates listed below and have also identified below any foreign application for patent or inventor's certificates having a filing date before that of the application on

Prior Foreign Applica	ation(s)				Priorit Claim	
(Number)	(Country)	(	Day/Month/Year	Filed)	Yes	No
(Number)	(Country)		Day/Month/Year	Filed)	Yes	No
(Number)	(Country)		Day/Month/Year	Filed)	Yes	No
application(s) listed to is not disclosed in the of Title 35, United St known to me to be m Section 1.56 which to	enefit under Title 35, Unite of the perior united States appares Code, Section 112, naterial to patentability a pecame available between attonal filing date of this	e subject ma olication in th I acknowled s defined in en the filing c	tter of each of the me manner provious ge the duty to d Fitle 37, Code of late of the prior	ne claims of ded by the fi isclose all ir f Federal Re	this ap irst par iformat gulatio	plication ragraph tion ons,
09/245,030	02/05	5/99	US Pat	. No. 6,207,	994	
(Application Seria		Date	(Status		<del></del>	
09/574,563	3 05/1	7/00	US Pat	. No. 6,570,	219	
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08/744,18			`	,		
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statements were m are punishable by f States Code and th	n information and belicade with the knowledgine or imprisonment, cat such willful false statent issued thereon.	e that willfu or both, und	I false stateme er Section 1001	nts and the I of Title 18	like s of the	o made
Full Name of Sole/Fi	irst Inventor	Vlad	limir Rumennik			
Inventor's Signature	Vladimir K	umeni	nk Date	01.31	09	<u>'</u>
Residence	Los Altos, California (City, State)		Citizenship	(Co	USA ountry)	
Post Office Address	27915 Roble Blanco C Los Altos Hills, CA 94					
Full Name of Second	d/Joint Inventor	Donal	d Ray Disney			<del></del>
Inventor's Signature	Joseph K 1) u	may	Date	1/27/	04	
Residence	Cupertino, California (City, State)		Citizenship		USA ountry)	
Post Office Address	10153 Colby Avenue Cupertino, CA 95014					

Full Name of Third/Jo	oint Inventor	anardhanan S. Ajit		
Inventor's Signature		Date		*
Residence	Sunnyvale, California (City, State)	Citizenship	India (Country)	
Post Office Address	455-E Costa Mesa Terrace Sunnyvale, CA 94086			



## APPENDIX A

Title 37, Code of Federal Regulations, Section 1.56 Duty to Disclose Information Material to Patentability

- (a) A patent by its very nature is affected with a public interest. The public interest is best served, and the most effective patent examination occurs when, at the time an application is being examined, the Office is aware of and evaluates the teachings of all information material to patentability. Each individual associated with the filing and prosecution of a patent application has a duty of candor and good faith in dealing with the Office, which includes a duty to disclose to the Office all information known to that individual to be material to patentability as defined in this section. The duty to disclose information exists with respect to each pending claim until the claim is cancelled or withdrawn from consideration becomes abandoned. Information material to the patentability of a claim that is cancelled or withdrawn from consideration ned not be submitted if the information is not material to the patentability of any claim remaining under consideration in the application. There is no duty to submit information which is not material to the patentability of any existing claim. The duty to disclose all information known to be material to patentability of any existing claim. The duty to disclose all information known to be material to patentability of any claim issued in a patent was cited by the Office or submitted to the Office in the manner prescribed by §§1.97(b)-(d) and 1.98. However, no patent will be granted on an application in connection with which fraud on the Office was practiced or attempted or the duty of disclosure was violated through bad faith or intentional misconduct. The Office encourages applicants to carefully examine:
- (1) Prior art cited in search reports of a foreign patent office in a counterpart application, and (2) The closest information over which individuals associated with the filing or prosecution of a patent application believe any pending claim patentably defines, to make sure that any material information contained therein is disclosed to the Office.
- (b) Under this section, information is material to patentability when it is not cumulative to information already of record or being made of record in the application, and
- (1) It establishes, by itself or in combination with other information, a prima facie case of unpatentability of a claim; or
  - (2) It refutes, or is inconsistent with, a position the applicant takes in:
    - (i) Opposing an argument of unpatentability relied on by the Office, or
      - (ii) Asserting an argument of patentability.

A prima facie case of unpatentability is established when the information compels a conclusion that a claim is unpatentable under the preponderance of evidence, burden-of-proof standard, giving each term in the claim its broadest reasonable construction consistent with the specification, and before any consideration is given to evidence which may be submitted in an attempt to establish a contrary conclusion of patentability.

- (c) Individuals associated with the filing or prosecution of a patent application within the meaning of this section are:
  - (1) Each inventor named in the application;
  - (2) Each attorney or agent who prepares or prosecutes the application; and
- (3) Every other person who is substantively involved in the preparation or prosecution of the application and who is associated with the inventor, with the assignee or with anyone to whom there is an obligation to assign the application.
- (d) Individuals other than the attorney, agent or inventor may comply with this section by disclosing information to the attorney, agent, or inventor.
- (e) In any continuation-in-part application, the duty under this section includes the duty to disclose to the Office all information known to the person to be material to patentability, as defined in paragraph (b) of this section, which became available between the filing date of the prior application and the national or PCT international filing date of the continuation-in-part application.